



By Jose L. Cuevas, 2004

I/O	Header	PIN#
Vcc(+5v)	AB.P1	1
Vcc(+5v)	AB.P1	12
Vcc(+5v)	AB.P2	1
Vcc(+5v)	AB.P2	12
I/O0	AB.P1	11
I/O1	AB.P1	13
I/O2	AB.P1	14
I/O3	AB.P1	15
I/O4	AB.P1	16
I/O5	AB.P1	17
I/O6	AB.P1	19
I/O7	AB.P1	20
I/O8	AB.P1	3
I/O9	AB.P1	4
I/O10	AB.P1	5
I/O11	AB.P2	5
I/O12	AB.P2	6
I/O13	AB.P1	10
I/O14	AB.P2	15
I/O15	AB.P2	16
GND	AB.P1	7
GND	AB.P1	18
GND	AB.P2	7
GND	AB.P2	18

I/O pins

- + 3.3v CMOS logic, 5v TTL level tolerant
- + 1.6mA sink or source

Power

- + DC 5v 100mA peak supplied from USB
- + DC 5v 300mA Max. supply from USB available to external circuit
- + Optional DC 5v external supply to header pin. (remove jumper PWR.J1 when using external power)

Size:

- + Size 3.3"x1.9"
- + Max height 0.5"
- + Mounting hole diameter 0.14" (for #4, #6 or M3 screws)